

ATT Unicycle SR-IOV Validation HW, Networking and IP plan

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Unicycle OVS-DPDK Validation Servers

HP

ProLiant DL380 Gen10

Dual socket Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz with 384GB RAM

HPE Smart Array P816i-a SR Gen10 with 6x2.4GB (Bay 1-6) and 4x480GB SSD (Bay 7-10)

PCI-E Card Layout:

PCI-E Slot 1	Empty slot 1
PCI-E Slot 2	HPE 12G SAS Expander Card
PCI-E Slot 3	Intel(R) Ethernet Controller XXV710 for 25GbE SFP28
PCI-E Slot 4	Empty slot 4
PCI-E Slot 5	Intel(R) Ethernet Controller XXV710 for 25GbE SFP28
PCI-E Slot 6	HPE Ethernet 10Gb 2-port 562SFP+ Adapter (not used but does impact device names and bus assignments)

Dell

<INSERT DIAGRAM WITH SPECIFIC NIC SLOTS>

Unicycle SR-IOV Validation Networking

The physical, L2, IP subnet, host and iDRAC/iLO addresses principles used in validation are the same as used in other validations of the same blueprint. Architectural details are shown here [Network Architecture](#) and details can be viewed from the pod specific input file [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on Dell 740XD Servers](#) and [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on HP DL380 Gen10 servers](#).

Unicycle SR-IOV Validation IP/VLAN Plan

Details of the IP address plan used in validation can be viewed from the pod specific input file [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on Dell 740XD Servers](#) and [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on HP DL380 Gen10 servers](#).

Unicycle SR-IOV BGP Plan

The unicycle nodes automatically peer with an external fabric BGP speaker using eBGP. The calico nodes do not peer using an internal iBGP mesh.

Details of the BGP plan used in validation can be viewed from the 'ksn' network section in the pod specific input file [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on Dell 740XD Servers](#) and [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on HP DL380 Gen10 servers](#).

Unicycle SR-IOV LAG Details

LAG details used in validation can be viewed in the pod specific input file [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on Dell 740XD Servers](#) and [Example Configuration Input File - Unicycle Pods with SR-IOV Dataplane on HP DL380 Gen10 servers](#).